

**IN THE DRAWINGS:**

Proposed corrected drawings are submitted herewith with changes marked in red in order to overcome the objections of the Official Draftsperson. It is noted that the unconnected lines in Figures 3 and 5 represent short and/or open circuits.

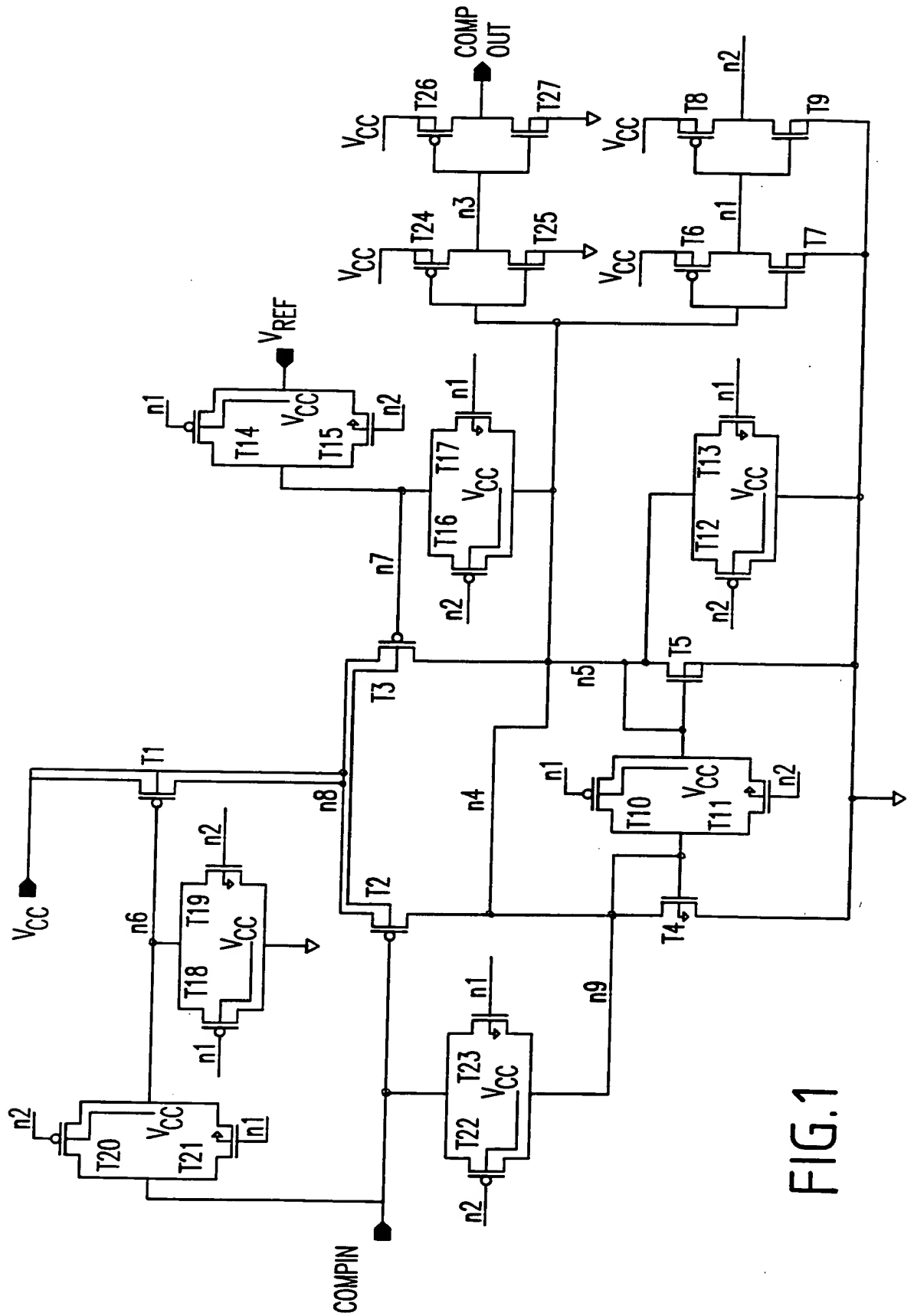


FIG.1

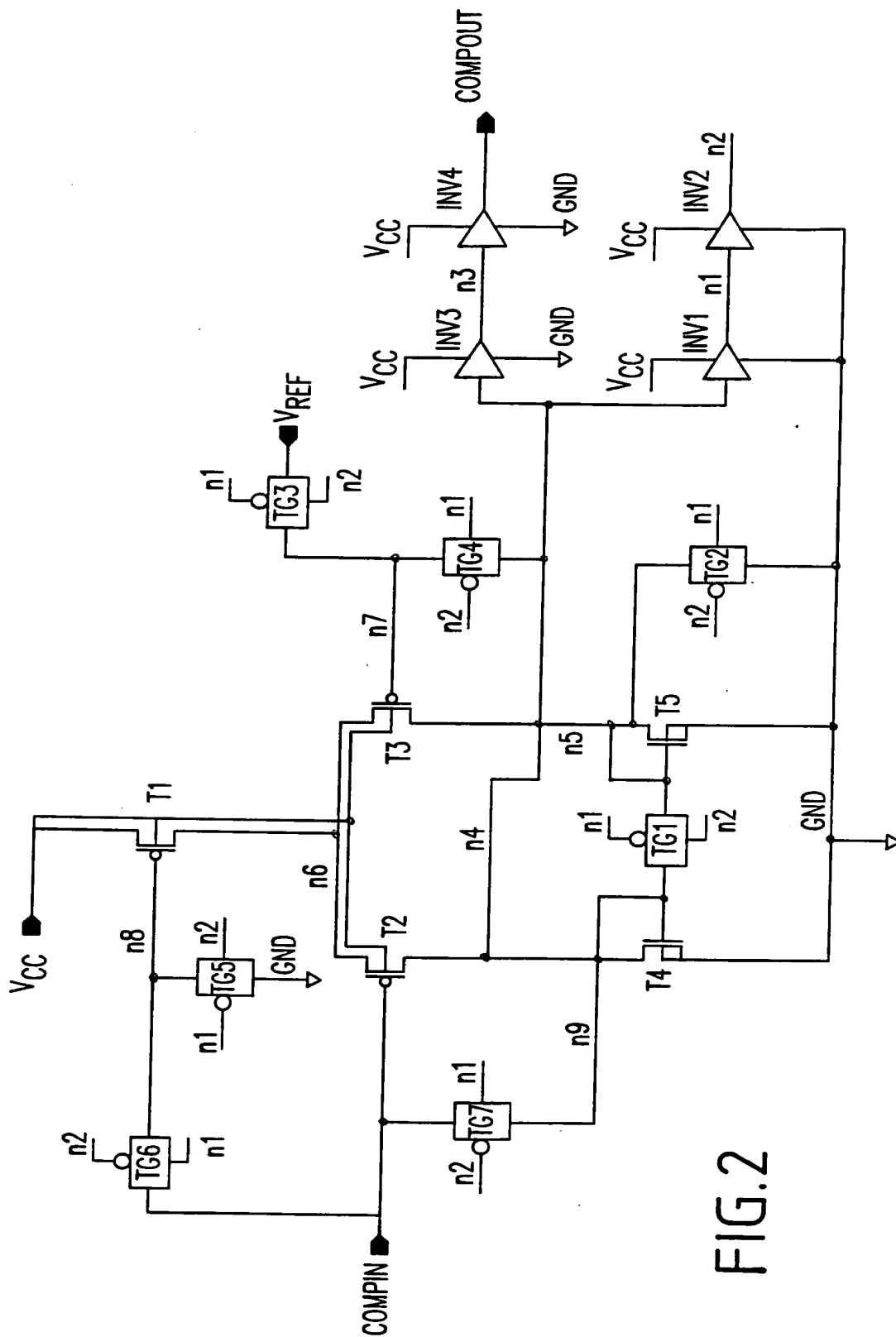


FIG. 2



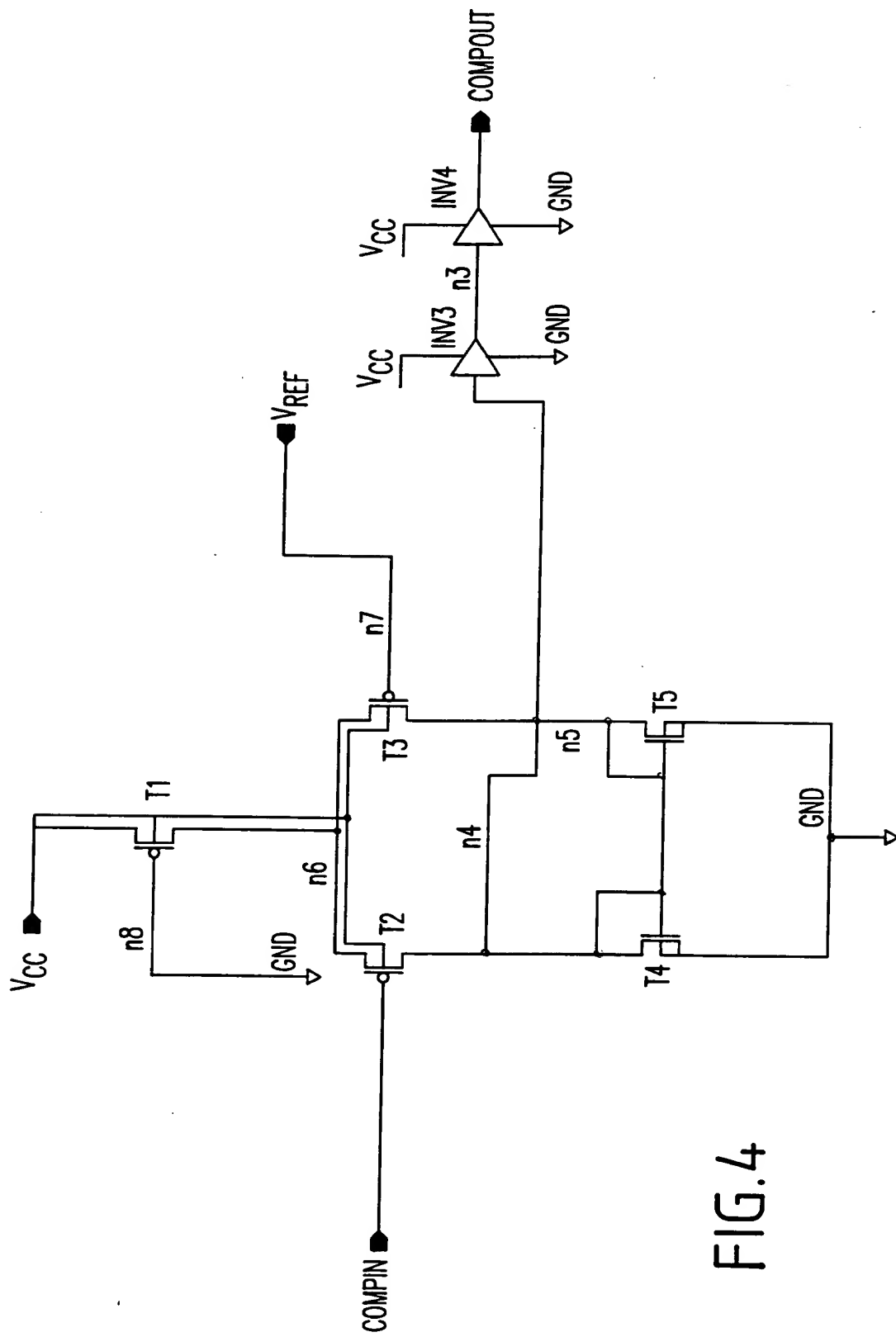


FIG.4

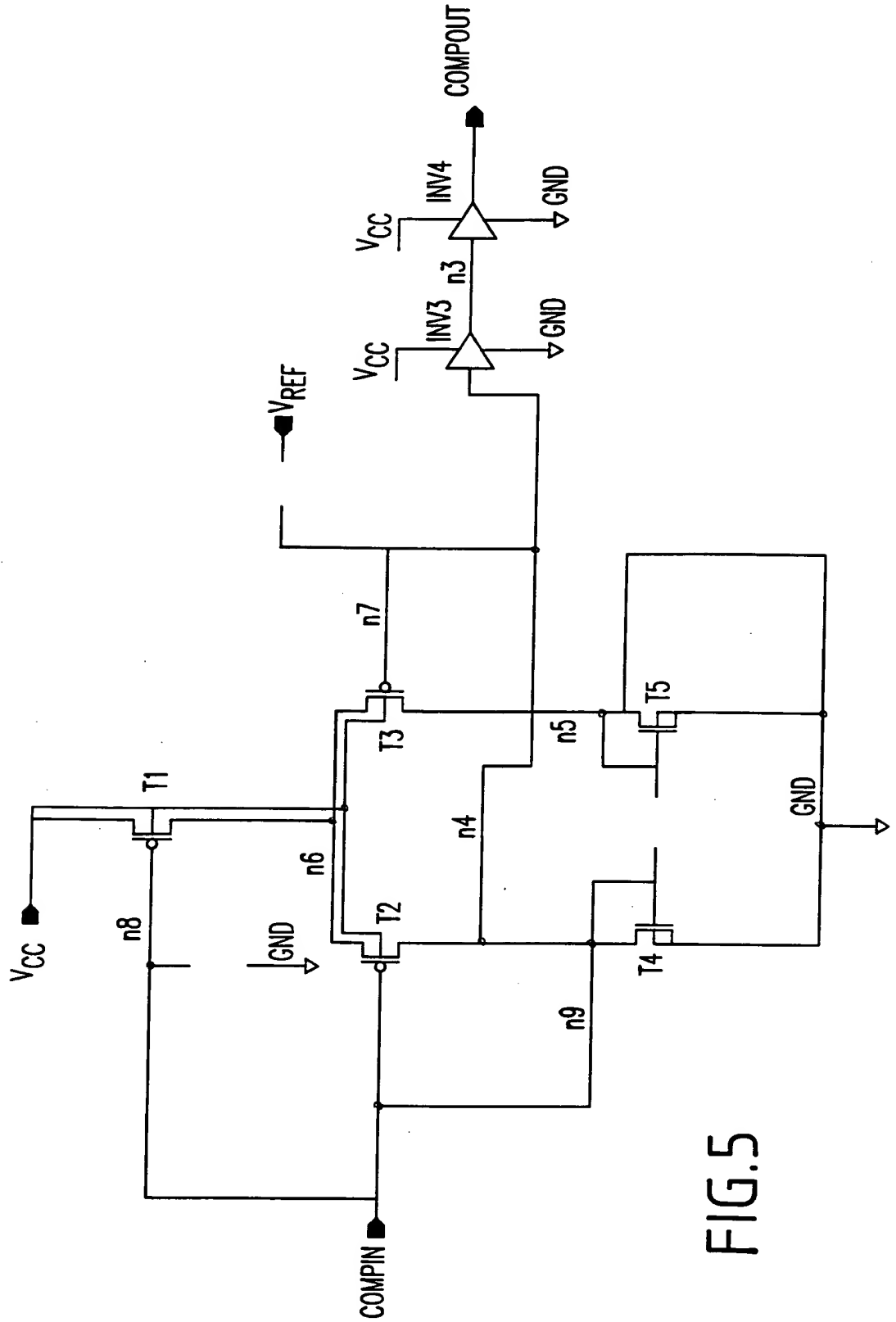


FIG.5

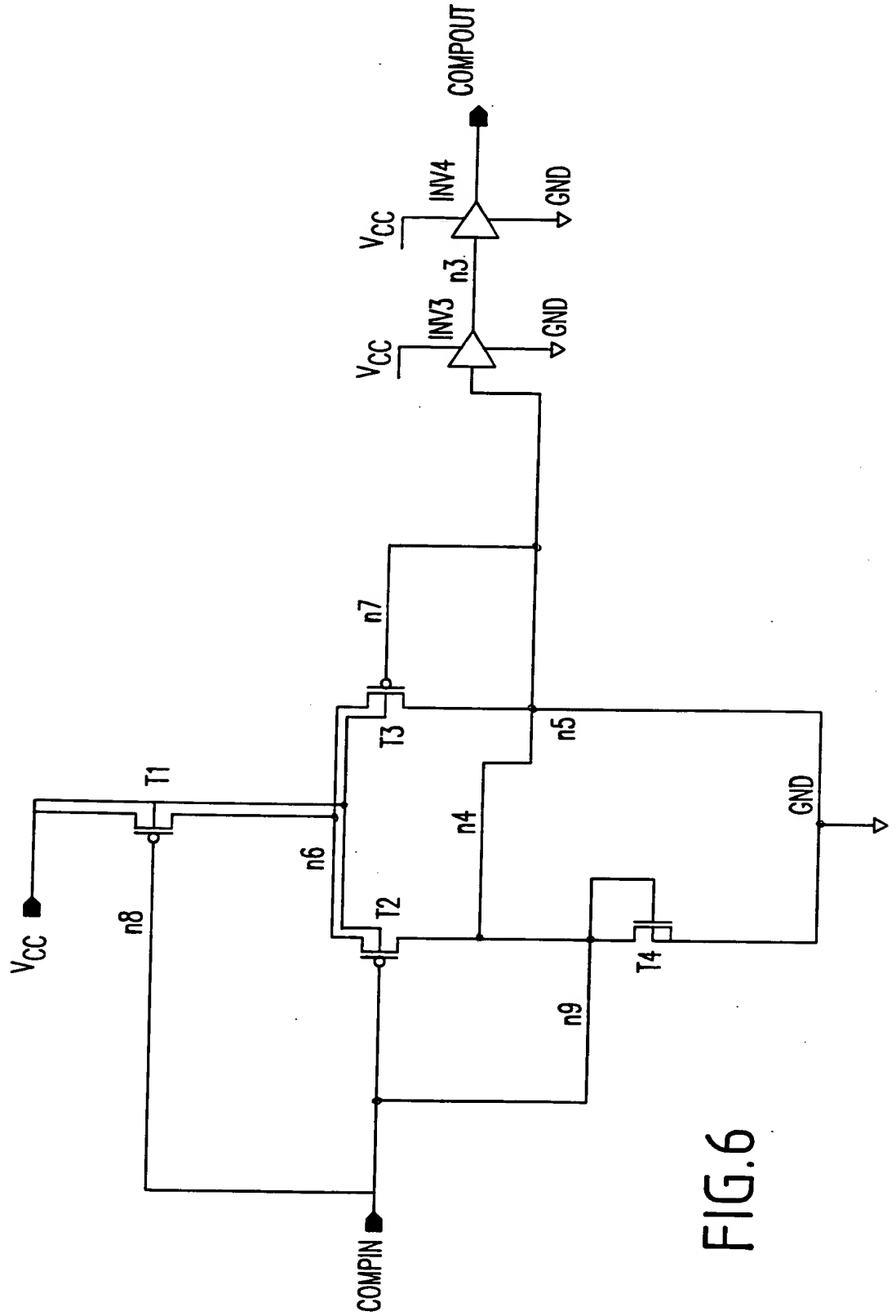
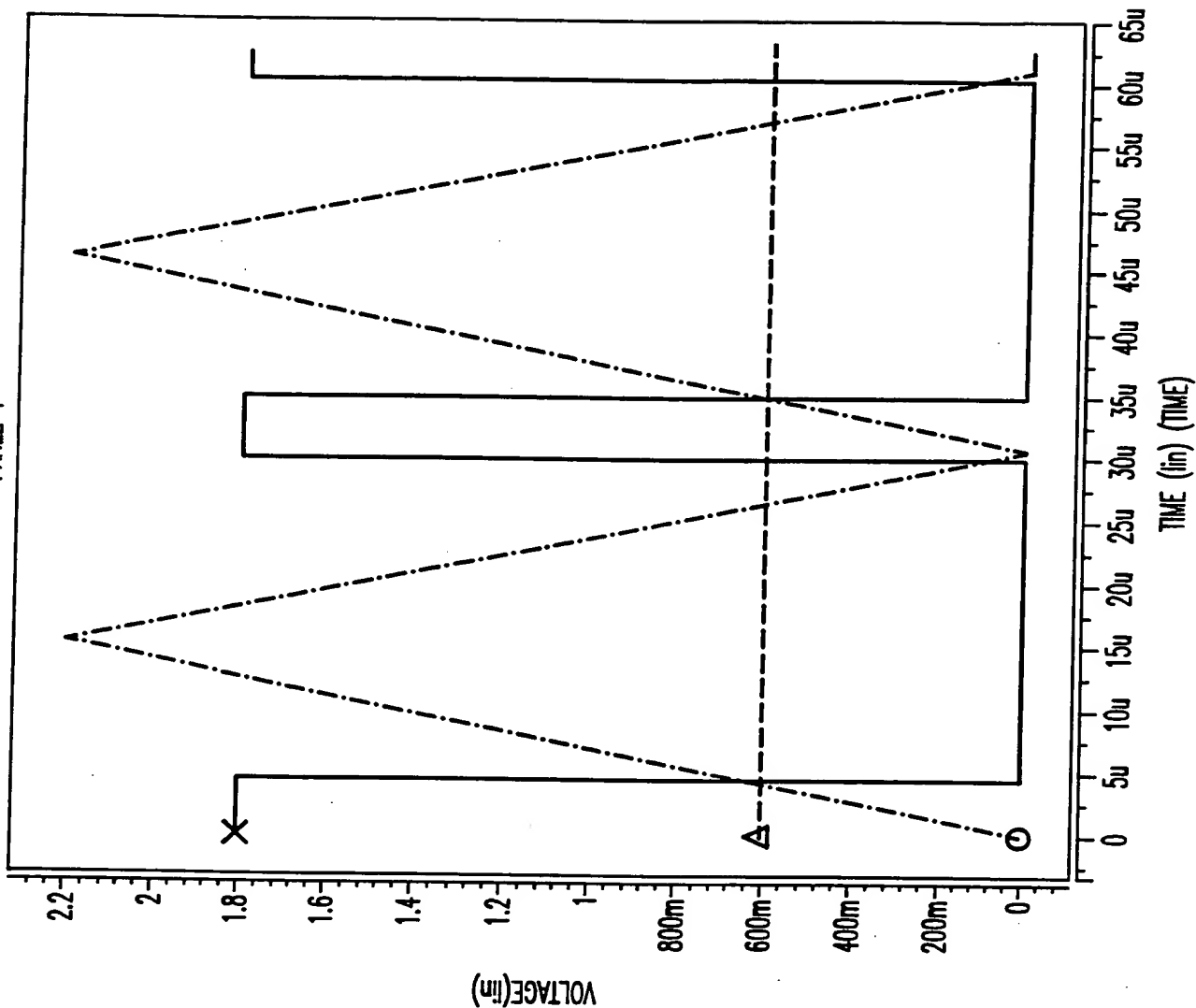


FIG.6



PANEL 1



WAVE	SYMBOL
DOADM(COMPIN)	○
DOADM(VREL)	△
DOADM(COMPOUT)	×

FIG.7



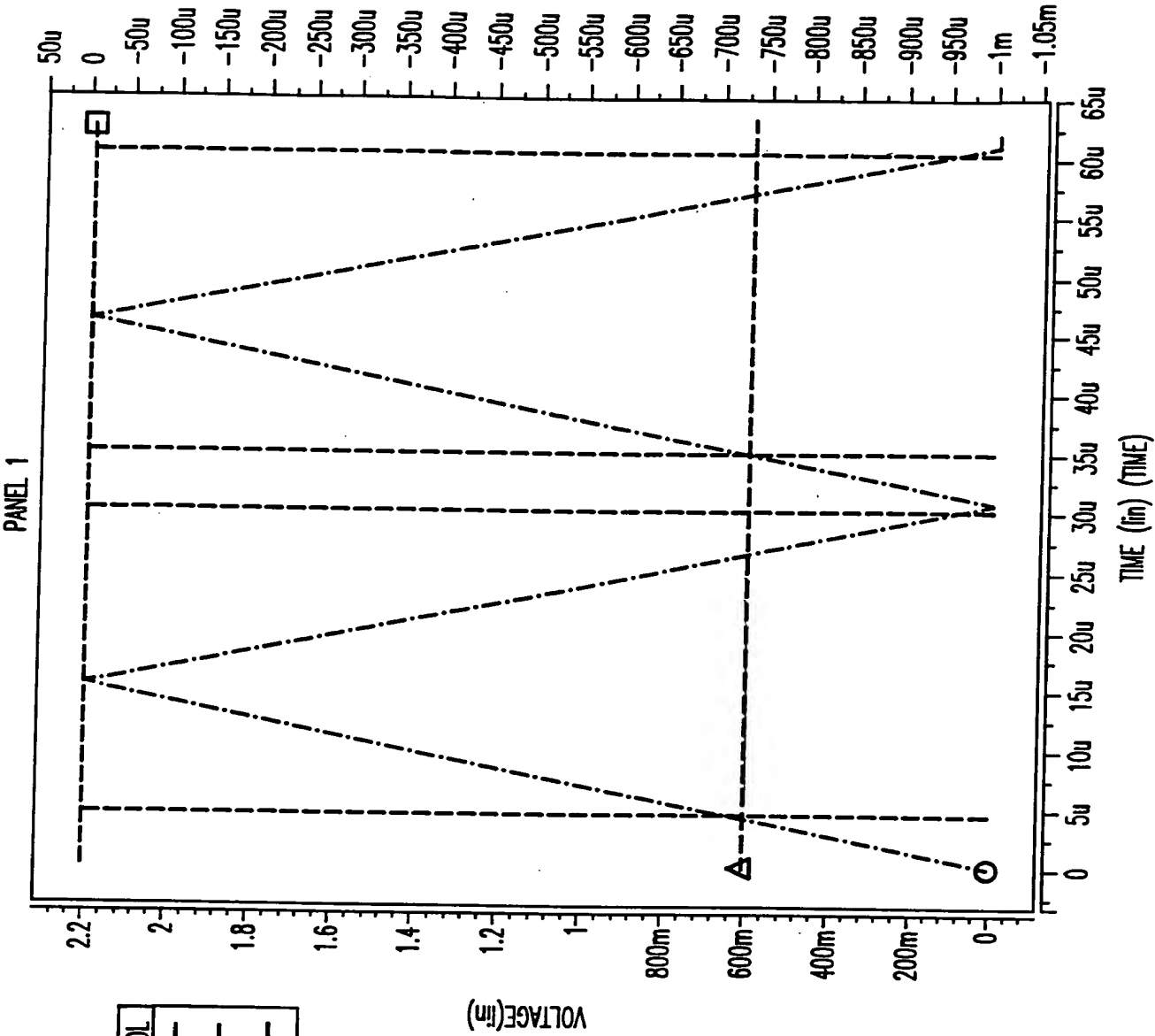


FIG.8

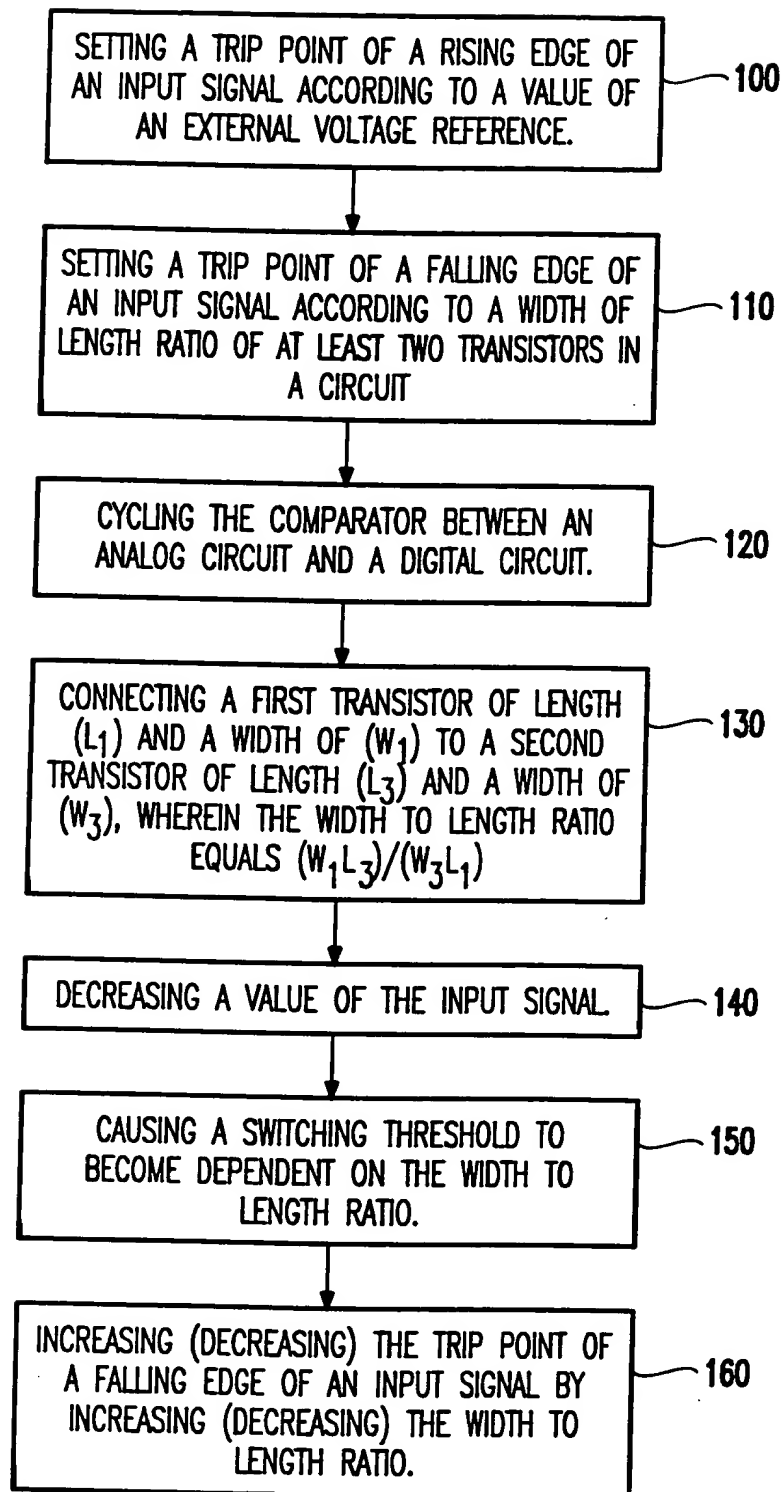
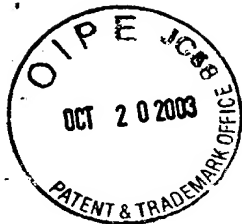


FIG.9